

PRODUCT INFORMATION

What's new in winIDEA 2009

Software

Upload while sampling

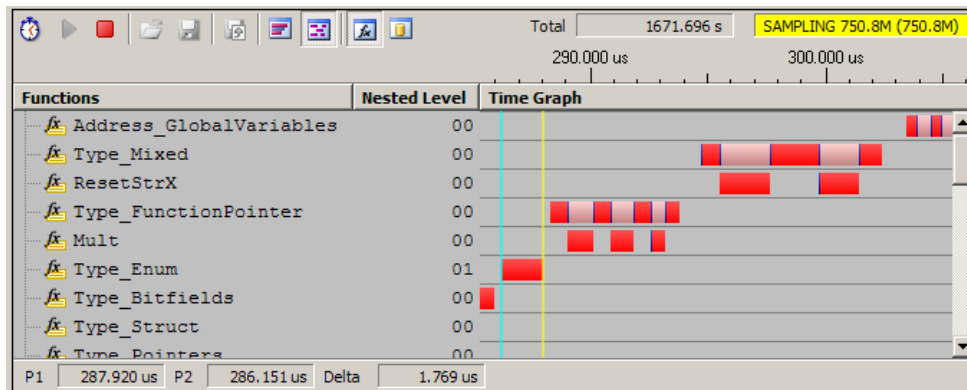
- Upload while sampling performance significantly improved on key OCT (on-chip trace) target platforms. Indefinite trace, profiler and coverage are now possible on key architectures.

Source Control

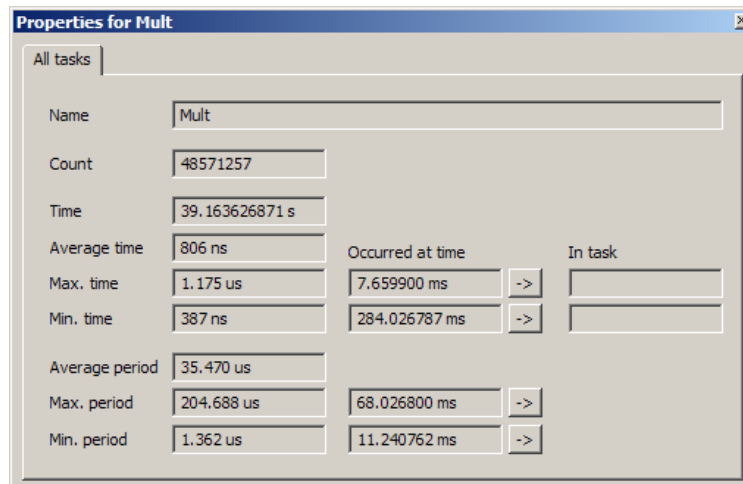
- Support for Subversion source control

Profiler

- New profiler progress bar



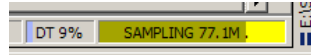
- Area properties now shows total plus per task results in individual pages
- Times of occurrence are displayed and 'goto' buttons are provided to scroll the window for min/max execution time and min/max period



- For min/max in the total result the offending task name is displayed
- Function areas can now have additional exit points and can ignore symbol table default exit points
- Support for shared exit points between multiple routines
- Improved profiler diagnostics
- Ignore unknown functions is now .PRD file specific, load last session is workspace specific.

Trace

- New trace progress bar



Eclipse Debug Plug-in

- Eclipse debug environment can now be used to debug the target application using iSYSTEM development tools.

Extendable Plug-in Interface

- MPC5xxx MMU & Cache plug-in
- Cortex timer plug-in (display of Cortex M3 cycle counters)
- Sciopta plug-in (kernel awareness for the Sciopta OS)

PowerPC Simulator

- Supported e200z0, e200z1, e200z3 and e200z6 core

Automatic Emulation Start on Power-on Detection

- winIDEA can be now configured for automatic emulation start when power-on is detected.

Build Manager

- Support for argument files

Build manager can now process compiler and assembler command scripts just like the linker does. All standard winIDEA macros can be used in these files.

- Project configuration file size reduction

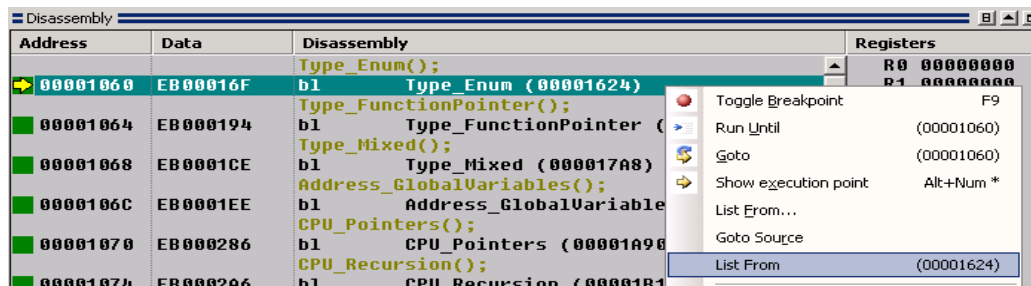
Only relevant and non-default options are saved to the xqrf file.

winIDEA command line build utility

- winIDEA xqrf projects can be built with a command line tool

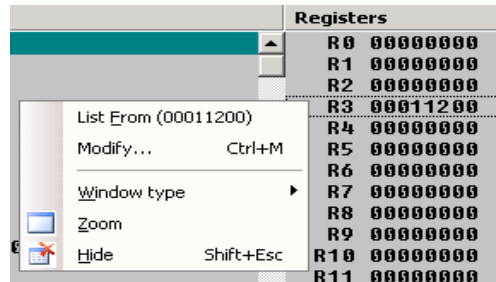
Disassembly window enhancements

- Context menu/List from direct branch target.



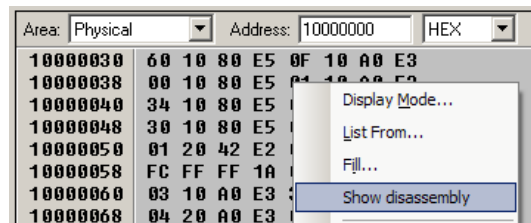
Disassembly register window enhancements

- Added list from register, modify register context menu
- Drag register to disassembly pane (list) or watch pane (add expression).



Memory window enhancements

- Added disassembly list from current position

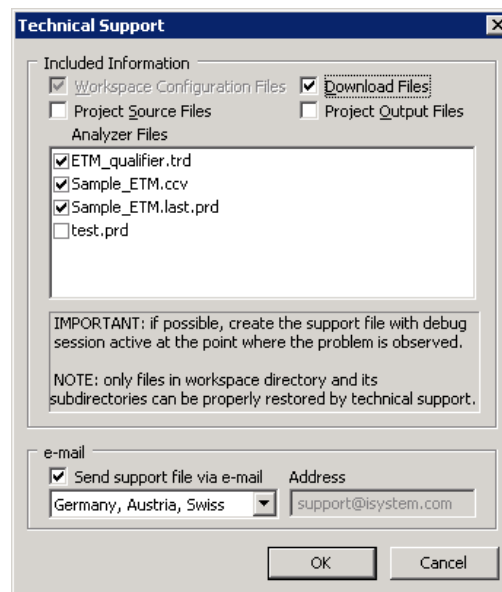


Memory Window Save format extensions

- For Intel HEx and Motorola S formats the number of bytes per line is configurable.

Tech support dialog changed

- All trace (.trd) and coverage (.ccv) files which are currently open are listed and checked for inclusion per default. All trace (.trd), profiler (.prd) and coverage (.ccv) files located in the workspace folder (but are not opened in winIDEA) are listed and unchecked for inclusion.



Other improvements

- Support for SFR access from gdbserver
- Implemented Microtek compiler filter
- Browser displays offsets of type elements
When browsing a structured type, offset from type start is displayed for the selected element.
- Support for multiple addresses per line symbol
In inline functions or GCC multiplied vtor instances a single source code line can generate code in several different locations. If a BP is set on such a line, winIDEA will set physical BPs on all these instances.
- iConnect & Automation access to gdbserver functionality
gdbserver can now be stopped/started, the architecture and the port configured using iConnect and the automation interface.
- iConnect and Automation access to 55xx Cache plug-in information
The information provided by the 55xx cache plug-in is available by using iConnect and/or automation.
- Added option to show/hide non coding lines from coverage reports
Description: The stat view can now optionally show/hide non coding lines.
- Added export flags to the automation server
It is now possible to export trace, coverage and profiler results using automation.
- iConnect communication to winIDEA plug-ins
winIDEA plug-in can be controlled via iConnect using IConnectIDE2::Service function.
- Project include paths and environment settings are now transferred in a readable format
Previously a compressed binary format was used which was unsuitable for external xqrf parsers/generators.
- Symbol window shows functions and globals defined for specific module
A module can be expanded to show functions and globals defined in that module.
- Progress dialog size persistent over sessions
The resizable progress dialog's size is now saved into workspace file. When first opened, it is always centered over winIDEA main window.
- WindRiver filter added to the filters list
- Improved code coverage
Coverage now shows non coding lines and assembler instructions, uses much less memory, a new export format is available for code review.
- Forced loading of CPU specific plug-ins
Description: Plug-ins which are tailored to current CPU are loaded automatically.
- New winIDEA setup utility

Hardware

Cortex-M3

- Debug support for ST STM32 family including trace support (SWO, TRACE, ETM)
- TMS470MSF54x (Hydra) Active GT POD

Cortex-R4

- Debug and trace support
- Time interpolation for better profiler accuracy

Freescal MPC55xx/551x/56xx

- Debug support for new devices including Nexus trace
- RTR trace available for default Power ISA instruction set on all devices excluding MPC551x and MPC56xx devices
- eTPU, eDMA and FlexRay trace supported on devices where applicable.
- BAM code trace
- Time interpolation for better profiler accuracy
- Profiler analysis optimized for speed



ST 56xx

- Debug support implemented including Nexus trace

ST STM8

- Debug support

Freescal MPC56x

- RTR Nexus trace available on iTRACE GT
- Nexus trace over reset supported

Freescal S12X

- ICE & BDM debug support for new devices
- Electrically isolated debug iCARD

Freescal S12

- New S12P derivatives supported

Freescal MC9S08

- BDM debug support for new devices.
- Electrically isolated debug iCARD

ColdFire

- New devices supported
- New ColdFire V1 family supported
- RTC (Real Time Compression) implemented on iTRACE GT allowing infinite trace and profiler
- Time interpolation for better profiler accuracy implemented
- Profiler analysis optimized for speed

- WDDATA trace supported for task profiling
- Unsecure flash support

Freescal 68332

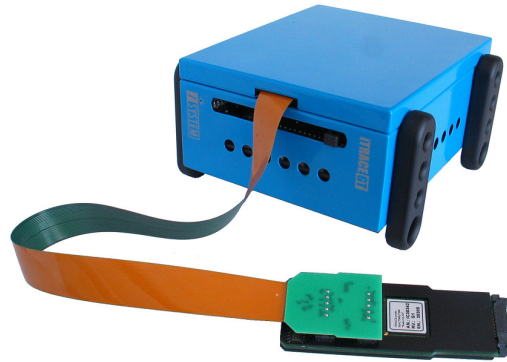
- 68332 ActiveGT POD
- MC68332 II Power POD

NEC V850

- Debug support for new devices

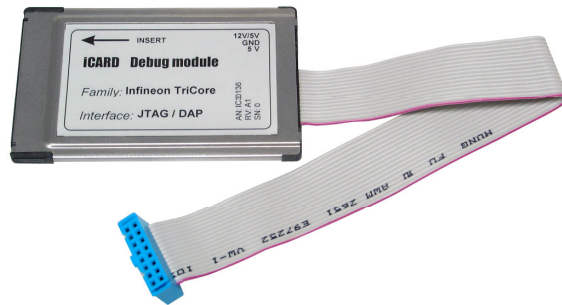
NEC 78K0

- Serial debug support
- EEPROM emulation supported on 78K0 ActivePRO PODs
- Data profiler implemented for 78K0 ActivePRO PODs



Infineon XC16x/XC2xxx

- Debug support for new devices including XC2000ED (Emulation Device) with the trace
- Custom devices can be visible to a specific customer only
- DAP debug interface supported
- Hot Attach implemented



Infineon TriCore

- Debug support for AUDO FUTURE devices including Emulation Device (XC1767ED, XC1797ED) with the trace
- Trace license introduced

Renesas R8C/3x

- Renesas R8C/3x ActivePRO POD

Guidant

- iTRACE GT support

External Flash devices

- New flash devices supported

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