
Adapters

IA40D20D1051

Target CPU package: DIP20
Body size: 0.97 in x 0.29 in
Pitch: 2.54 mm

Can be used with:

- 8031 POD (IC81025-30)
- H-80C52 POD
- H-8xC52 POD

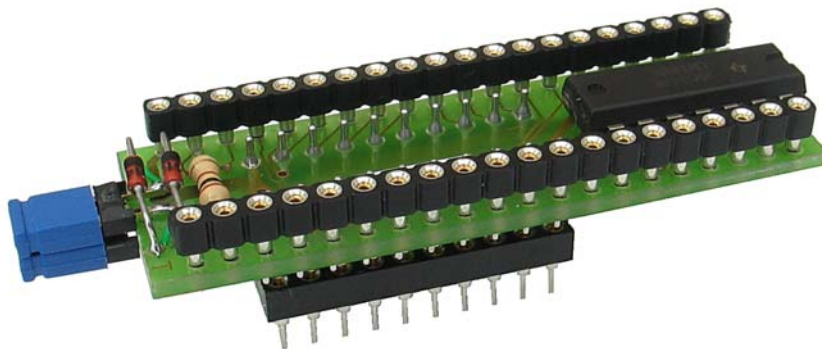
Note that adapter solutions stated in the document can be used only with listed PODs. Disregarding this warning may result in hardware failure of the target and the emulation system.

The IA40D20D1051 adapter is used only when the POD emulates AT89C1051, AT89C2051 and AT89C4051 CPUs. All three listed CPUs are single-chip CPUs and don't have P2 and P0.

Ports P1.0 and P1.1 are inputs to the analog comparator and output from the comparator is internally connected to P3.6, which is not available (externally) on these CPUs.

Ports P1.0 and P1.1 can be used as I/O ports too. When used as I/O ports, the two jumpers must be set, otherwise not. If the adapter has no jumpers, it means it's an old version and P1.0 and P1.1 can operate only as inputs to the analog comparator. Contact iSYSTEM representative for upgrade possibility.

Listed CPUs are emulated by standard 8031 CPUs. Analog comparator and connection to P3.6 are implemented on the IA40D20D1051 adapter.



Assembled IA40D20D1051 adapter

While assembling the adapter and connecting the POD to the target, pay attention to pin 1 to prevent any damages of the hardware, which may result from incorrect assembly.

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